

REMARKS

Entry of this amendment, reconsideration of all grounds of objection and rejection, and allowance of the pending claims are respectfully requested in light of the above amendments and the following remarks. Claim 2-7 remain pending herein. Claims 1 and 8-9 have been canceled hereby without prejudice or disclaimer. Claims 4, 5 (as amended) and claim 6 are independent claims.

At the outset, Applicant notes with appreciation the indication that claims 6-7 are allowed and that claims 4 and 5 recite allowable subject matter. Applicants have rewritten claims 4 and 5 in an independent format including all the recitation of their base claim. Accordingly Applicant respectfully submits that claims 4 and 5 are now in condition for allowance.

In addition, Applicant has amended claim 2 to depend from claim 4, which renders claim 2 allowable at least for dependency from allowable claim 4. Claim 3, which further depends from claim 2, is believed allowable for similar reasons as well.

Claims 1-3, 8 and 9 stand rejected under 35 U.S.C. §102(b) as being anticipated by Ausserlechner *et al.* (U.S. 6,559,721). Applicant respectfully submits that the cancellation of claims 1, 8 and 9 render this ground of rejection moot.

Finally, the drawings were objected to because the recited "source-drain diodes in anti-series" are said to be not shown. Applicant, in a effort to comply, has edited FIG. 1a, for example, to show the anti-series flow between transistors 17 and 18. As disclosed by the Applicants in the specification (paragraph [0011] of U.S. 2007/0159229 for this application), "the connection deviates from conventional drain-drain or source-source

coupling of complementary transistors. As a result, the source-drain diodes of the transistors are in anti-series".

In referring to the above, Applicant respectfully submits that in the art, when one refers to "source-drain diode", it is understood from looking at a schematic of the MOS transistor, as the MOS transistor, a three terminal device, can be considered to have a construction of two diodes.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. A Notice of Allowance is respectfully requested.

Respectfully submitted,

Aaron Waxler
Registration No. 48,027



By: Steve Cha
Attorney for Applicant
Registration No. 44,069

Date: September 15, 2008

Mail all correspondence to:

Aaron Waxler, Registration No. 48,027
NXP, B.V.
NXP Intellectual Property Department
M/S41-SJ
1109 McKay Drive
San Jose, CA 95131
Phone: (408) 434-3000
Fax: (408) 474-9081

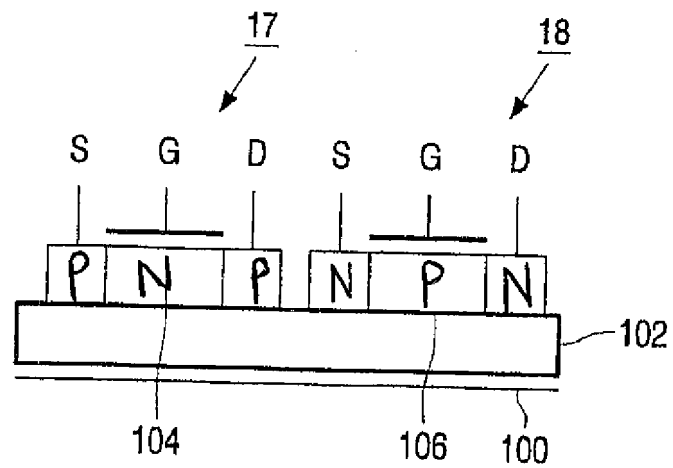
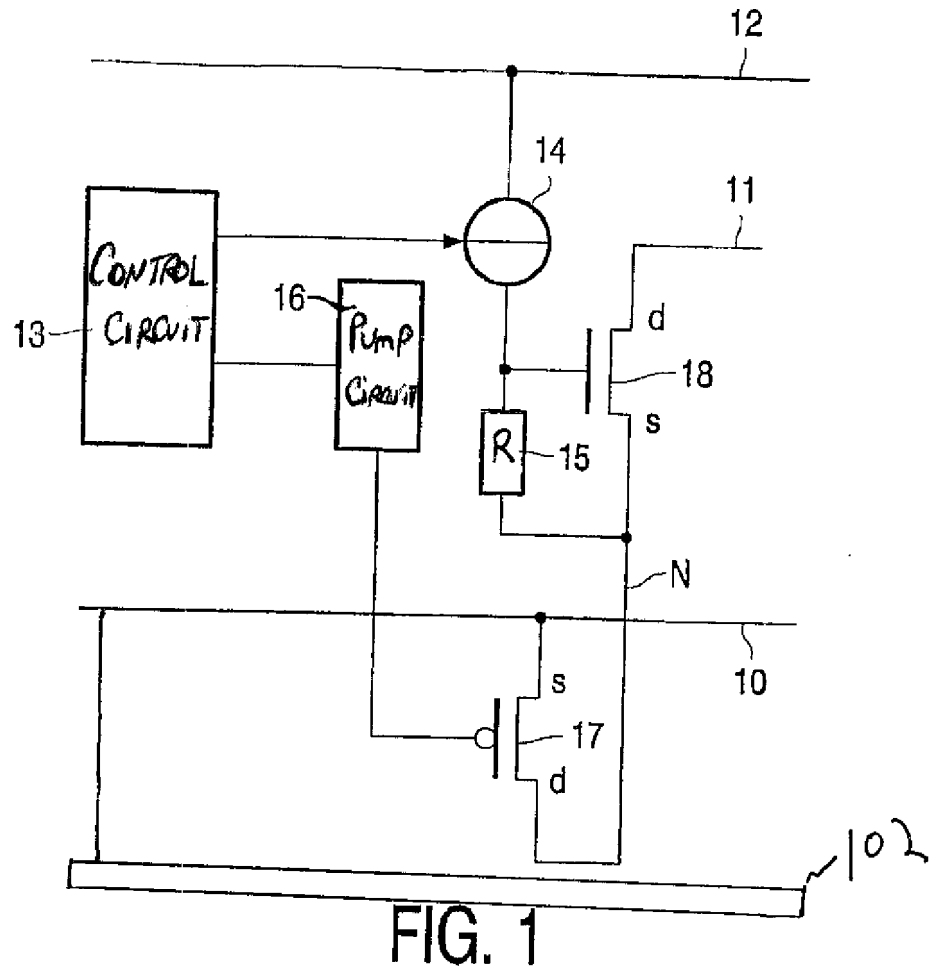


FIG. 1a